

PATENT APPLICATION
Docket No. 5484-103
Client No. PX1508-US/SSD

17853 US PTO
10/612593
07/01/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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	DATE OF DEPOSIT: <u>JULY 1, 2003</u>
I HEREBY CERTIFY THAT THIS PAPER AND ENCLOSURES AND/OR FEE ARE BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE "EXPRESS MAIL POST OFFICE TO ADDRESSEE" SERVICE UNDER 37 CFR 1.10 ON THE DATE INDICATED ABOVE AND IS ADDRESSED TO: MAIL STOP PATENT APPLICATION, COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450.	
<u>JILL JEANS</u> (SENDER'S PRINTED NAME)	<u>Jill Jeans</u> (SIGNATURE)

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor : Jong-Hyun CHOI and Jae-Hoon KIM

For: CIRCUIT AND METHOD FOR SELECTING REFERENCE
VOLTAGES IN SEMICONDUCTOR MEMORY DEVICE

Applicant requests FIG. 2 to be published with the application.

Enclosures:

- ☒ Specification (pages 1-11); claims (pages 12-15); abstract (page 16)
- ☒ FOUR sheet(s) of FORMAL drawings (comprising four figures)
- ☒ Combined Declaration and Power of Attorney
- ☒ Newly executed (original or copy)
- ☐ Assignment with cover sheet

Assignee Name and Address: Samsung Electronics Co.
Suwon-city, Kyungki-do
Republic of Korea

CLAIMS AS FILED				
For	Number Filed	Number Extra	Rate	Basic Fee \$750
Total Claims	16-20	0	x \$ 18 =	\$0.00
Independent Claims	5-3	2	x \$ 84 =	\$168.00
TOTAL FILING FEE				\$918.00

- ☒ Certified copy of Priority Document No. 2002-38009, filed July 2, 2002
- ☒ PTO 1449 Information Disclosure Statement (with 3 cited references)
- ☒ Return Postcard
- ☒ PTO Form 2038 authorizing credit card payment for \$918.00 the above-listed fees filing fee (\$750.00) and extra claims fee (\$168.00) is enclosed.
- ☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.



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PATENT TRADEMARK OFFICE

Respectfully submitted,

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